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1   A stateless, content-directed data prefetching mechanism   ■

Robert Cooksey, Stephan Jourdan, Dirk Grunwald
October 2002 **Proceedings of the 10th international conference on Architectural support for programming languages and operating systems**, Volume 37 , 30 , 36 Issue 10 , 5 , 5

Full text available: 📄 pdf(1.20 MB)   Additional Information: full citation, abstract, references, citings

Although central processor speeds continues to improve, improvements in overall system performance are increasingly hampered by memory latency, especially for pointer-intensive applications. To counter this loss of performance, numerous data and instruction prefetch mechanisms have been proposed. Recently, several proposals have posited a *memory-side* prefetcher; typically, these prefetchers involve a distinct processor that executes a program slice that would effectively prefetch data nee ...

2   The interaction of knowledge sources in word sense disambiguation   ■

Mark Stevenson, Yorick Wilks
September 2001 **Computational Linguistics**, Volume 27 Issue 3

Full text available: 📄 pdf(2.16 MB) 📰   Additional Information: full citation, abstract, references
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Word sense disambiguation (WSD) is a computational linguistics task likely to benefit from the tradition of combining different knowledge sources in artificial in telligence research. An important step in the exploration of this hypothesis is to determine which linguistic knowledge sources are most useful and whether their combination leads to improved results. We present a sense tagger which uses several knowledge sources. Tested accuracy exceeds 94% on our evaluation corpus.Our system attempts ...

3   System-level power optimization: techniques and tools   ■

Luca Benini, Giovanni de Micheli
April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

Full text available: 📄 pdf(385.22 KB)   Additional Information: full citation, abstract, references, citings, index terms

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic sytems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy

consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survery ...

4 Exokernel: an operating system architecture for application-level resource management

D. R. Engler, M. F. Kaashoek, J. O'Toole
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5
Full text available: pdf(2.16 MB)   Additional Information: full citation, references, citings, index terms

5 Reduction of latency and resource usage in bit-level pipelined data paths for FPGAs

P. Kollig, B. M. Al-Hashimi
February 1999 **Proceedings of the 1999 ACM/SIGDA seventh international symposium on Field programmable gate arrays**
Full text available: pdf(1.36 MB)   Additional Information: full citation, references, index terms

Keywords: FPGA, bit-level pipelined, circuit latency, recursive algorithms

6 YAPI: application modeling for signal processing systems

E. A. de Kock, W. J. M. Smits, P. van der Wolf, J.-Y. Brunel, W. M. Kruijtzer, P. Lieverse, K. A. Vissers, G. Essink
June 2000 **Proceedings of the 37th conference on Design automation**

Full text available: pdf(94.62 KB)   Additional Information: full citation, abstract, references, citings, index terms

We present a programming interface called YAPI to model signal processing applications as process networks. The purpose of YAPI is to enable the reuse of signal processing applications and the mapping of signal processing applications onto heterogeneous systems that contain hardware and software components. To this end, YAPI separates the concerns of the application programmer, who determines the functionality of the system, and the system designer, who determines the implementation of the ...

Keywords: Khan process networks, application modeling, model of computation, signal processing, systems-level design

7 Managing energy and server resources in hosting centers

Jeffrey S. Chase, Darrell C. Anderson, Prachi N. Thakar, Amin M. Vahdat, Ronald P. Doyle
October 2001 **ACM SIGOPS Operating Systems Review , Proceedings of the eighteenth ACM symposium on Operating systems principles**, Volume 35 Issue 5

Full text available: pdf(1.61 MB)   Additional Information: full citation, abstract, references, citings, index terms

Internet hosting centers serve multiple service sites from a common hardware base. This paper presents the design and implementation of an architecture for resource management in a hosting center operating system, with an emphasis on *energy* as a driving resource management issue for large server clusters. The goals are to provision server resources for co-hosted services in a way that automatically adapts to offered load, improve the energy efficiency of server clusters by dynamically res ...

8 A QoS adaptive transport system: design, implementation and experience

Andrew Campbell, Geoff Coulson
February 1997 **Proceedings of the fourth ACM international conference on Multimedia**
Full text available: pdf(1.29 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

9 Power minimization in IC design: principles and applications

Massoud Pedram
January 1996 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 1 Issue 1
Full text available: pdf(550.02 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Low power has emerged as a principal theme in today's electronics industry. The need for low power has caused a major paradigm shift in which power dissipation is as important as performance and area. This article presents an in-depth survey of CAD methodologies and techniques for designing low power digital CMOS circuits and systems and describes the many issues facing designers at architectural, logical, and physical levels of design abstraction. It reviews some of the techniques and tool ...

Keywords: CMOS circuits, adiabatic circuits, computer-aided design of VLSI, dynamic power dissipation, energy-delay product, gated clocks, layout, low power layout, low power synthesis, lower-power design, power analysis and estimation, power management, power minimization and management, probabilistic analysis, silicon-on-insulator technology, statistical sampling, switched capacitance, switching activity, symbolic simulation, synthesis, system design

10 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren
November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**
Full text available: pdf(4.21 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

11 Precision and error analysis of MATLAB applications during automated hardware synthesis for FPGAs

A. Nayak, M. Haldar, A. Choudhary, P. Banerjee
March 2001 **Proceedings of the conference on Design, automation and test in Europe**
Full text available: pdf(121.48 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

12 Asymptotic resource consumption in multicast reservation styles

Danny J. Mitzel, Scott Shenker
October 1994 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Communications architectures, protocols and applications,** Volume 24 Issue 4
Full text available: pdf(881.32 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The goal of network design is to meet the needs of resident applications in an efficient manner. Adding real-time service and point-to-multipoint multicast routing to the Internet's traditional point-to-point best effort service model will greatly increase the Internet's efficiency in handling point-to-multipoint real-time applications. Recently, the RSVP resource reservation protocol has introduced the concept of "reservation styles", which control how reservations are aggregat ...

13 Synthesis of low power folded programmable coefficient FIR digital filters (short paper)

Vijay Sundararajan, Keshab K. Parhi
January 2000　**Proceedings of the 2000 conference on Asia South Pacific design automation**
Full text available: ⬛ pdf(151.33 KB)　Additional Information: full citation, references

14 Session 9B: Power issues in high level synthesis: An integrated data path optimization for low power based on network flow method

Chun Gi Lyuh, Taewhan Kim, C. L. Liu
November 2001　**Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**
Full text available: ⬛ pdf(305.30 KB)　Additional Information: full citation, abstract, references, citings, index terms

We propose an effective algorithm for power optimization in behavioral synthesis. In previous work, it has been shown that several hardware allocation/binding problems for power optimization can be formulated as network flow problems and be solved optimally. However, in these formulations, a fixed schedule was assumed. In such context, one key problem is: given an optimal network flow solution to a hardware allocation/binding problem for a schedule, how to generate a new optimal network flow sol ...

15 Mobile Code and Distributed Systems: The performance of public key-enabled kerberos authentication in mobile computing applications

Alan Harbitter, Daniel A. Menascé
November 2001　**Proceedings of the 8th ACM conference on Computer and Communications Security**
Full text available: ⬛ pdf(419.31 KB)　Additional Information: full citation, abstract, references, citings, index terms

Authenticating mobile computing users can require a significant amount of processing and communications resources-particularly when protocols based on public key encryption are invoked. These resource requirements can result in unacceptable response times for the user. In this paper, we analyze adaptations of the public key-enabled Kerberos network authentication protocol to a mobile platform by measuring the service time of a "skeleton" implementation and constructing a closed queuing network m ...

Keywords: authentication, kerberos, mobile computing, performance modeling, proxy servers, public key cryptography

16 Positional adaptation of processors: application to energy reduction

Michael C. Huang, Jose Renau, Josep Torrellas
May 2003　**ACM SIGARCH Computer Architecture News , Proceedings of the 30th annual international symposium on Computer architecture**, Volume 31 Issue 2
Full text available: ⬛ pdf(225.57 KB)　Additional Information: full citation, abstract, references, citings

Although adaptive processors can exploit application variability to improve performance or save energy, effectively managing their adaptivity is challenging. To address this problem,

we introduce a new approach to adaptivity: the *Positional* approach. In this approach, both the *testing* of configurations and the *application* of the chosen configurations are associated with particular code sections. This is in contrast to the currently-used *Temporal* approach to adaptation ...

17 Configuration cloning: exploiting regularity in dynamic DSP architectures

S. R. Park, W. Burleson
February 1999 **Proceedings of the 1999 ACM/SIGDA seventh international symposium on Field programmable gate arrays**
Full text available: pdf(1.72 MB)   Additional Information: full citation, references, citings, index terms

18 Managing battery lifetime with energy-aware adaptation

Jason Flinn, M. Satyanarayanan
May 2004 **ACM Transactions on Computer Systems (TOCS)**, Volume 22 Issue 2
Full text available: pdf(1.61 MB)   Additional Information: full citation, abstract, references, index terms

We demonstrate that a collaborative relationship between the operating system and applications can be used to meet user-specified goals for battery duration. We first describe a novel profiling-based approach for accurately measuring application and system energy consumption. We then show how applications can dynamically modify their behavior to conserve energy. We extend the Linux operating system to yield battery lifetimes of user-specified duration. By monitoring energy supply and demand and ...

Keywords: Power management, adaptation

19 Poster session: Lattice adaptive filter implementation for FPGA

Zdenek Pohl, Rudolf Matoušek, Jirí Kadlec, Milan Tichý, Miroslav Lícko
February 2003 **Proceedings of the 2003 ACM/SIGDA eleventh international symposium on Field programmable gate arrays**
Full text available: pdf(187.05 KB)   Additional Information: full citation, abstract

Our poster introduces an innovative RLS Lattice filter implementation for FPGAs. The signal processing applications typically require wide numeric range, and that poses a problem when using an FPGA implementation. Our approach is based on arithmetic using logarithmic numeric representation (LNS). The test application - an adaptive noise canceller - has been optimized for the Xilinx Virtex devices. It consumes roughly 70% of all logic resources of the XCV800 device and all block memory cells. The ...

20 Guessing morphology from terms and corpora

Christian Jacquemin
July 1997 **ACM SIGIR Forum , Proceedings of the 20th annual international ACM SIGIR conference on Research and development in information retrieval**,
Volume 31 Issue SI
Full text available: pdf(1.62 MB)   Additional Information: full citation, references, citings, index terms

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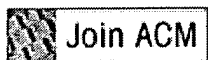ACM Transactions on Computer Systems (TOCS)
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1 **A Novel Channel-Identification Method for Wireless Communication Systems**
Xu, H.; Dasgupta, S.; Ding, Z.;
Communications, IEEE Transactions on , Volume: 52 , Issue: 10 , Oct. 2004
Pages:1767 - 1776

[Abstract]   [PDF Full-Text (448 KB)]   **IEEE JNL**

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2 **A complexity reduction of ETSI advanced front-end for DSR**
Jin-Yu Li; Bo Liu; Ren-Hua Wang; Li-Rong Dai;
Acoustics, Speech, and Signal Processing, 2004. Proceedings. (ICASSP '04). IE International Conference on , Volume: 1 , 17-21 May 2004
Pages:I - 61-4 vol.1

[Abstract]   [PDF Full-Text (332 KB)]   **IEEE CNF**

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3 **An improved feedback scheme for dual channel identification in wire communication systems**
Honghui Xu; Dasgupta, S.; Zhi Ding;
Acoustics, Speech, and Signal Processing, 2003. Proceedings. (ICASSP '03). 2 IEEE International Conference on , Volume: 4 , 6-10 April 2003
Pages:IV - 77-80 vol.4

[Abstract]   [PDF Full-Text (349 KB)]   **IEEE CNF**

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4 **ETSI AMR-2 VAD: evaluation and ultra low-resource implementation**
Cornu, E.; Sheikhzadeh, H.; Brennan, R.L.; Abutalebi, H.R.; Tam, E.C.Y.; Iles, Wong, K.W.;
Acoustics, Speech, and Signal Processing, 2003. Proceedings. (ICASSP '03). 2 IEEE International Conference on , Volume: 2 , 6-10 April 2003

Pages:II - 585-8 vol.2

[Abstract]   [PDF Full-Text (348 KB)]   IEEE CNF

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5 An ultra low power, ultra miniature voice command system based on hidden Markov models

Cornu, E.; Destrez, N.; Dufaux, A.; Sheikhzadeh, H.; Brennan, R.;
Acoustics, Speech, and Signal Processing, 2002. Proceedings. (ICASSP '02). IE
International Conference on , Volume: 4 , 13-17 May 2002
Pages:IV-3800 - IV-3803 vol.4

[Abstract]   [PDF Full-Text (434 KB)]   IEEE CNF

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6 Case study: medical Web service for the automatic 3D documentatio neuroradiological diagnosis

Iserhardt-Bauer, S.; Hastreiter, P.; Ertl, T.; Eberhardt, K.; Tomandl, B.;
Visualization, 2001. VIS '01. Proceedings , 21-26 Oct. 2001
Pages:425 - 581

[Abstract]   [PDF Full-Text (390 KB)]   IEEE CNF

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